

	L #	H it	S arch Text	DB
1	L1	1144	(361/785,790,803).ccls.	USP AT; US-P GPU B
2	L2	3046	(257/685,686,691,723,77).ccls.	USP AT; US-P GPU B
3	L3	426	438/109.cccls.	USP AT; US-P GPU B
4	L4	792	(439/69,74).cccls.	USP AT; US-P GPU B
5	L5	4986	1 2 3 4	USP AT; US-P GPU B
6	L6	550	5 and @pd>=20030120	USP AT; US-P GPU B
7	L7	388	6 and (ground power signal\$4)	USP AT; US-P GPU B

	L #	Hit	Search T xt	DB
8	L8	162	7 and (slow\$2 fast\$2 speed\$1)	USP AT; US-P GPU B
9	L9	76	8 and (connector\$1 socket\$1)	USP AT; US-P GPU B
10	L10	86	8 not 9	USP AT; US-P GPU B